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UNITED STATES SECURITIES AND EXCHANGE COMMISSION Washington, D.C. 20549

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STATEMENT OF CHANGES IN BENEFICIAL OWNERSHIP OF SECURITIES

Filed pursuant to Section 16(a) of the Securities Exchange Act of 1934 or Section 30(h) of the Investment Company Act of 1940

Instruction 1(b) (Print or Type Responses) 5. Relationship of Reporting Person(s) to Issuer 1. Name and Address of Reporting Person * 2. Issuer Name and Ticker or Trading Symbol ANDERSON JAMES ROBERT (Check all applicable) ADVANCED MICRO DEVICES INC [AMD] Director 10% Owner (Middle) 3. Date of Earliest Transaction (Month/Day/Year) Officer (give title below SVP & GM, CGBG 07/15/2016

Other (specify below) ADVANCED MICRO DEVICES, INC., ONE AMD PLACE (Street) 4. If Amendment, Date Original Filed(Month/Day/Year) 6. Individual or Joint/Group Filing(Check Applicable Line) X_Form filed by One Reporting Person
Form filed by More than One Reporting Person SUNNYVALE, CA 94088-3453 (Zip) (City) (State) Table I - Non-Derivative Securities Acquired, Disposed of, or Beneficially Owned 1. Title of Security 2. Transaction 2A. Deemed 5. Amount of Securities Beneficially 7. Nature 3. Transaction 4. Securities Acquired Owned Following Reported of Indirect Code (Instr. 3) Date Execution Date, if (A) or Disposed of (D) Ownership (Month/Day/Year) (Instr. 8) (Instr. 3, 4 and 5) Transaction(s) Form: Beneficial (Month/Day/Year (Instr. 3 and 4) Direct (D) Ownership or Indirect (Instr. 4) (A) or (I) Code Amount (D) Price (Instr. 4)

Reminder: Report on a separate line for each class of securities beneficially owned directly or indirectly

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SEC 1474 (9-02)

Table II - Derivative Securities Acquired, Disposed of, or Beneficially Owned (e.g., puts, calls, warrants, options, convertible securities)

	1. Title of	2.	3. Transaction	3A. Deemed	4.		5. Number of		6. Date Exercisable		7. Title and Amount of		8. Price of	9. Number of	10.	11. Nature
	Derivative	Conversion	Date	Execution Date, if	Transact	ransaction Derivative		and Expiration Date		Underlying Securities		Derivative	Derivative	Ownership	of Indirect	
	Security	or Exercise	(Month/Day/Year)	any	Code	Securities		(Month/Day/Year)		(Instr. 3 and 4)		Security	Securities	Form of	Beneficial	
	(Instr. 3)	Price of		(Month/Day/Year)	(Instr. 8)	8) Acquired (A)						(Instr. 5)	Beneficially	Derivative	Ownership	
		Derivative					or Disposed of							Owned	Security:	(Instr. 4)
		Security				(D)							Following	Direct (D)		
						(Instr. 3, 4,							Reported	or Indirect		
						and 5)							Transaction(s)	(I)		
												Amount or		(Instr. 4)	(Instr. 4)	
										Expiration		Number of				
					Code	V	(A)	(D)	Exercisable	Date		Shares				
Ī	RSU	(1)	07/15/2016				00.573		(2)	(2)	Common	00.572.00	Ф.О	00.572	D	
	Award	(1)	07/15/2016		Α		98,572		(2)	(2)	Stock	98,572.00	\$ 0	98,572	D	
	PRSU	(2)	07/15/2016				107.145		(4)	(4)	Common	197,145.00	Φ.0	107.145	Ъ	
	Award	(3)	07/15/2016		Α		197,145		<u>(4)</u>	(4)	Stock	197,145.00	\$ 0	197,145	D	

Reporting Owners

Danielia - Oroman Nama / Addusa	Relationships							
Reporting Owner Name / Address	Director	10% Owner	Officer	Other				
ANDERSON JAMES ROBERT ADVANCED MICRO DEVICES, INC. ONE AMD PLACE SUNNYVALE, CA 94088-3453			SVP & GM, CGBG					

Signatures

07/18/2016 /s/ James Robert Anderson Signature of Reporting Person Date

Explanation of Responses:

- If the form is filed by more than one reporting person, see Instruction 4(b)(v).
- ** Intentional misstatements or omissions of facts constitute Federal Criminal Violations. See 18 U.S.C. 1001 and 15 U.S.C. 78ff(a).
- (1) Each restricted stock unit ("RSU") represents a contingent right to receive one share of AMD's common stock.
- (2) The RSUs vest 1/3 each on 8/9/2017, 8/9/2018 and 8/9/2019.

- Each performance-based restricted stock unit ("PRSU") represents a contingent right to receive between 0% and 250% of one share of AMD's common stock. The resulting number of shares (3) acquired upon vesting of the PRSU is contingent upon three-year compound annual growth rate milestones related to AMD's closing stock price that may be attained with the three-year performance period commencing on July 15, 2016 and ending on August 9, 2019 (the "Performance Period").
 - The number of PRSUs that may be earned, if at all, is based on three-year compound annual growth rate milestones related to AMD's closing stock price that may be attained within the Performance Period, with the potential payout levels of PRSUs at 50%, 100%, 150%, 200% and 250% of the target number of PRSUs granted. Any PRSU earned pursuant to the attainment of
- (4) a performance level will vest and be paid out 50% upon the Compensation Committee's certification of the attainment of the performance level (provided, that no PRSU will vest before the first anniversary of the grant date) and the remaining 50% will vest and be paid out at the end of the Performance Period, subject to the recipient's continuous employment or service through each such vesting date.

Note: File three copies of this Form, one of which must be manually signed. If space is insufficient, see Instruction 6 for procedure.

Potential persons who are to respond to the collection of information contained in this form are not required to respond unless the form displays a currently valid OMB number.