

(Print or Type Responses)

1. Name and Address of Reporting Person * Papermaster Mark D			2. Issuer Name and Ticker or Trading Symbol ADVANCED MICRO DEVICES INC [AMD]			5. Relationship of Reporting Person(s) to Issuer (Check all applicable) ____ Director ____ 10% Owner <input checked="" type="checkbox"/> Officer (give title below) _____ Chief Technology Officer & SVP			
(Last) (First) (Middle) ONE AMD PLACE			3. Date of Earliest Transaction (Month/Day/Year) 08/03/2017						
(Street) SUNNYVALE, CA 94085			4. If Amendment, Date Original Filed(Month/Day/Year)			6. Individual or Joint/Group Filing(Check Applicable Line) <input checked="" type="checkbox"/> Form filed by One Reporting Person ____ Form filed by More than One Reporting Person			
(City) (State) (Zip)			Table I - Non-Derivative Securities Acquired, Disposed of, or Beneficially Owned						
1. Title of Security (Instr. 3)	2. Transaction Date (Month/Day/Year)	2A. Deemed Execution Date, if any (Month/Day/Year)	3. Transaction Code (Instr. 8)		4. Securities Acquired (A) or Disposed of (D) (Instr. 3, 4 and 5)		5. Amount of Securities Beneficially Owned Following Reported Transaction(s) (Instr. 3 and 4)	6. Ownership Form: Direct (D) or Indirect (I) (Instr. 4)	7. Nature of Indirect Beneficial Ownership (Instr. 4)
			Code	V	Amount	(A) or (D) Price			
Common Stock	08/03/2017		M		246,430	A \$ 0	1,086,522	D	
Common Stock	08/03/2017		S		126,438 (1)	D \$ 13.18 (2)	960,084	D	

Reminder: Report on a separate line for each class of securities beneficially owned directly or indirectly.

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Table II - Derivative Securities Acquired, Disposed of, or Beneficially Owned (e.g., puts, calls, warrants, options, convertible securities)														
1. Title of Derivative Security (Instr. 3)	2. Conversion or Exercise Price of Derivative Security	3. Transaction Date (Month/Day/Year)	3A. Deemed Execution Date, if any (Month/Day/Year)	4. Transaction Code (Instr. 8)		5. Number of Derivative Securities Acquired (A) or Disposed of (D) (Instr. 3, 4, and 5)	6. Date Exercisable and Expiration Date (Month/Day/Year)		7. Title and Amount of Underlying Securities (Instr. 3 and 4)		8. Price of Derivative Security (Instr. 5)	9. Number of Derivative Securities Beneficially Owned Following Reported Transaction(s) (Instr. 4)	10. Ownership Form of Derivative Security: Direct (D) or Indirect (I) (Instr. 4)	11. Nature of Indirect Beneficial Ownership (Instr. 4)
Performance Stock Units	\$ 0 <u>(3)</u>	08/03/2017		M		246,430 <u>(4)</u>	<u>(5)</u>	<u>(5)</u>	Common Stock	246,430.00	\$ 0	246,432 <u>(6)</u>	D	

Reporting Owners

Reporting Owner Name / Address	Relationships			
	Director	10% Owner	Officer	Other
Papermaster Mark D ONE AMD PLACE SUNNYVALE, CA 94085			Chief Technology Officer & SVP	

Signatures

/s/ Mark D. Papermaster

08/07/2017

Signature of Reporting Person

Date

Explanation of Responses:

* If the form is filed by more than one reporting person, see Instruction 4(b)(v).

** Intentional misstatements or omissions of facts constitute Federal Criminal Violations. See 18 U.S.C. 1001 and 15 U.S.C. 78ff(a).

(1) Represents shares automatically sold pursuant to an irrevocable sell to cover election to satisfy tax withholding obligations in connection with the Reporting Person's Performance-based Restricted Stock Unit ("PRSU") vesting.

Transaction executed in multiple trades at prices ranging from \$13.03 to \$13.35 per share, inclusive. The price reported in column 4 above reflects the weighted average sale price per share. The

(2) Reporting Person hereby undertakes to provide the SEC staff, the Issuer or a security holder of the Issuer, upon request, full information regarding the number of shares sold at each respective price within the range set forth in this footnote.

- Each PRSU represents a contingent right to receive between 0% and 250% of one share of AMD's common stock. The resulting number of shares acquired upon vesting of the PRSU is contingent
- (3) upon three-year compound annual growth rate milestones related to AMD's closing stock price that may be attained with the three-year performance period commencing on July 15, 2016 and ending on August 9, 2019 (the "Performance Period").
- (4) The reported number of securities represents 50% of the earned number of shares subject to the 2016 PRSUs, that were issuable on the date the Compensation and Leadership Resources Committee (the "Compensation Committee") certified performance.
- The number of PRSUs that may be earned, if at all, is based on three-year compound annual growth rate milestones related to AMD's closing stock price that may be attained within the Performance Period, with the potential payout levels of PRSUs at 50%, 100%, 150%, 200% and 250% of the target number of PRSUs granted. Any PRSU earned pursuant to the attainment of a
- (5) performance level will vest and be paid out 50% upon the Compensation Committee's certification of the attainment of the performance level (provided, that no PRSU will vest before the first anniversary of the grant date) and the remaining 50% will vest and be paid out at the end of the Performance Period, subject to the recipient's continuous employment or service through each such vesting date.
- (6) The reported number of securities represents the remaining 50% of the number of shares that will vest at the end of the Performance Period subject to the Reporting Person's continued employment.

Note: File three copies of this Form, one of which must be manually signed. If space is insufficient, *see* Instruction 6 for procedure.

Potential persons who are to respond to the collection of information contained in this form are not required to respond unless the form displays a currently valid OMB number.