

UNITED STATES SECURITIES AND EXCHANGE COMMISSION Washington, D.C. 20549

OMB APPROVAL

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Check this box if no longer subject to Section 16. Form 4 or Form 5 obligations may continue. *See* Instruction 1(b).

STATEMENT OF CHANGES IN BENEFICIAL OWNERSHIP OF SECURITIES

Filed pursuant to Section 16(a) of the Securities Exchange Act of 1934 or Section 30(h) of the Investment Company Act of 1940

(Print or Type Response	s)											
1. Name and Address of Reporting Person *- Su Lisa T			2. Issuer Name and Ticker or Trading Symbol ADVANCED MICRO DEVICES INC [AMD]						5. Relationship of Reporting Person(s) to Issuer (Check all applicable) X Director 10% Owner			
ADVANCED MIC AMD PLACE	RO DEVICES, INC	ONE	3. Date of Earliest Transaction (Month/Day/Year) 07/15/2016					X_ Officer (give title below) Other (specify below) President & CEO				
(Street) SUNNYVALE, CA 94088-3453			4. If Amendment, Date Original Filed(Month/Day/Year)						Individual or Joint/Group Filing/Check Applicable Line) _X_Form filed by One Reporting Person Form filed by More than One Reporting Person			
(City)	(State)	(Zip)	Table I - Non-Derivative Securities Acquired, Disposed of, or Beneficially Owned									
(Instr. 3) Date		2. Transaction Date (Month/Day/Year)	Execution Date, if	Code					5. Amount of Securities Beneficially Owned Following Reported Transaction(s) (Instr. 3 and 4)		7. Nature of Indirect Beneficial Ownership	
				Code	V	Amount	(A) or (D)	Price		or Indirect (I) (Instr. 4)	(Instr. 4)	
Reminder: Report on a	separate line for each cla	ass of securities ber	neficially owned direc	ctly or indire	ectly.							

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SEC 1474 (9-02)

Table II - Derivative Securities Acquired, Disposed of, or Beneficially Owned (e.g., puts, calls, warrants, options, convertible securities)

Persons who respond to the collection of information contained

1. Title of	1 -	3. Transaction	3A. Deemed	4.		5. Number		6. Date Exer		7. Title and			9. Number of		11. Nature
Derivative	Conversion	Date	Execution Date, if	Transaction Der		Derivative		and Expiration Date		Underlying Securities		Derivative	Derivative	Ownership	of Indirect
Security	or Exercise	(Month/Day/Year)	any	Code		Securities		(Month/Day	/Year)	(Instr. 3 and	4)	Security	Securities	Form of	Beneficial
(Instr. 3)	Price of		(Month/Day/Year)	(Instr. 8))	Acquired (A)					(Instr. 5)	Beneficially	Derivative	Ownership
	Derivative					or Dispose	d of						Owned	Security:	(Instr. 4)
	Security					(D)							Following	Direct (D)	
						(Instr. 3, 4,							Reported	or Indirect	
						and 5)							Transaction(s)	(I)	
													(Instr. 4)	(Instr. 4)	
								Date	Expiration	Title	Amount or Number of		, ,	· ·	
				C- 1-	v	(4)	(D)	Exercisable	Date						
				Code	V	(A)	(D)				Shares				
RSU	(1)	07/15/2016				245.004		<u>(2)</u>	(2)	Common	345,004.00	Φ.0	245.004	ъ	
Award	<u>(1)</u>	07/15/2016		Α		345,004		(4).	(4)	Stock	345,004.00	\$ 0	345,004	D	
PRSU	<u>(3)</u>	07/15/2016		۸		690,008		<u>(4)</u>	<u>(4)</u>	Common	690,008.00	\$ 0	690,008	D	
Award	(2)	07/13/2010		Α		050,008		(2).	X = J.	Stock	050,008.00	\$0	090,008	D	

Reporting Owners

Reporting Owner Name / Address	Relationships						
Reporting Owner Name / Address	Director	10% Owner	Officer	Other			
Su Lisa T ADVANCED MICRO DEVICES, INC. ONE AMD PLACE SUNNYVALE, CA 94088-3453	X		President & CEO				

Signatures

/s/ Lisa T. Su	07/18/2016
Signature of Reporting Person	Date

Explanation of Responses:

- * If the form is filed by more than one reporting person, see Instruction 4(b)(v).
- ** Intentional misstatements or omissions of facts constitute Federal Criminal Violations. See 18 U.S.C. 1001 and 15 U.S.C. 78ff(a).
- (1) Each restricted stock unit ("RSU") represents a contingent right to receive one share of AMD's common stock.
- (2) The RSUs will vest 1/3 each on 8/9/2017, 8/9/2018 and 8/9/2019.
- (3) Each performance-based restricted stock unit ("PRSU") represents a contingent right to receive between 0% and 250% of one share of AMD's common stock. The resulting number of shares acquired upon vesting of the PRSU is contingent upon three-year compound annual growth rate milestones related to AMD's closing stock price that may be attained with the three-year

performance period commencing on July 15, 2016 and ending on August 9, 2019 (the "Performance Period").

The number of PRSUs that may be earned, if at all, is based on three-year compound annual growth rate milestones related to AMD's closing stock price that may be attained within the Performance Period, with the potential payout levels of PRSUs at 50%, 100%, 150%, 200% and 250% of the target number of PRSUs granted. Any PRSU earned pursuant to the attainment of a performance level will vest and be paid out 50% upon the Compensation Committee's certification of the attainment of the performance level (provided, that no PRSU will vest before the first

(4) performance level will vest and be paid out 50% upon the Compensation Committee's certification of the attainment of the performance level (provided, that no PRSU will vest before the first anniversary of the grant date) and the remaining 50% will vest and be paid out at the end of the Performance Period, subject to the recipient's continuous employment or service through each such vesting date.

Note: File three copies of this Form, one of which must be manually signed. If space is insufficient, see Instruction 6 for procedure.

Potential persons who are to respond to the collection of information contained in this form are not required to respond unless the form displays a currently valid OMB number.