FORM 4

UNITED STATES SECURITIES AND EXCHANGE COMMISSION Washington, D.C. 20549

OMB APPROVAL
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STATEMENT OF CHANGES IN BENEFICIAL OWNERSHIP OF SECURITIES

Filed pursuant to Section 16(a) of the Securities Exchange Act of 1934 or Section 30(h) of the Investment Company Act of 1940

(Print or Type Responses)													
1. Name and Address of Rep KUMAR DEVINDER		2. Issuer Name and Ticker or Trading Symbol ADVANCED MICRO DEVICES INC [AMD]						5. Relationship of Reporting Person(s) to Issuer (Check all applicable) Director 10% Owner					
ONE AMD PLACE	(First)		3. Date of Earliest Transaction (Month/Day/Year) 08/09/2017						X_ Officer (give title below) Other (specify below) SVP, CFO & Treasurer				
SUNNYVALE, CA 940	4	4. If Amendment, Date Original Filed(Month/Day/Year)						6. Individual or Joint/Group Filing(Check Applicable Line) X_Form filed by One Reporting Person Form filed by More than One Reporting Person					
(City)	(State)	(Zip)	Table I - Non-Derivative Securities Acquired, Disposed of, or Beneficially Owned										
1.Title of Security (Instr. 3)		2. Transaction Date (Month/Day/Year)		Code (Instr. 8)		4. Securities Acquired (A) or Disposed of (D) (Instr. 3, 4 and 5)		of (D)	5. Amount of Securities Beneficially Owned Following Reported Transaction(s)	Form:	7. Nature of Indirect Beneficial		
			(Month/Day/Year)	Code	V	Amount	(A) or (D)	Price	(Instr. 3 and 4)		Ownership (Instr. 4)		
Common Stock		08/09/2017		M		73,513	A	\$ 0	793,403	D			
Common Stock		08/10/2017		S		39,507 (1)	D	\$ 12.46 (2)	753,896	D			

Reminder: Report on a separate line for each class of securities beneficially owned directly or indirectly.

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Table II - Derivative Securities Acquired, Disposed of, or Beneficially Owned (e.g., puts, calls, warrants, options, convertible securities)

1. Title of Derivative Security (Instr. 3)	Conversion	Date (Month/Day/Year)	Execution Date, if any	4. Transact Code (Instr. 8)	tion)			(Month/Day/Year)		Underlying Securities		Derivative Security (Instr. 5)	Securities Beneficially Owned Following	Ownership Form of Derivative Security: Direct (D)	11. Nature of Indirect Beneficial Ownership (Instr. 4)
				Code	V	(A)	(D)	Date Exercisable	Expiration Date	Title	Amount or Number of Shares		Transaction(s)	or Indirect (I) (Instr. 4)	
Restricted Stock Units	.(3)	08/09/2017		M			40,659	(4)	(4).	Common Stock	40,659.00	\$ 0	0	D	
Restricted Stock Units	.(3)	08/09/2017		M			32,854	(5)	<u>(5)</u> .	Common Stock	32,854.00	\$ 0	65,718	D	
Stock Option Grant	\$ 12.83	08/09/2017		A		86,894		<u>(6)</u>	08/09/2024	Common Stock	86,894.00	\$ 0	86,894	D	
RSU Award	(3)	08/09/2017		A		36,982		.(7)	(7).	Common Stock	36,982.00	\$ 0	36,982	D	
PRSU Award	(8)	08/09/2017		A		73,964		.(2)	(2).	Common Stock	73,964.00	\$ 0	73,964	D	

Reporting Owners

Reporting Owner Name / Address	Relationships							
Reporting Owner Name / Address	Director	10% Owner	Officer	Other				
KUMAR DEVINDER ONE AMD PLACE SUNNYVALE, CA 94085			SVP, CFO & Treasurer					

Signatures

/s/Devinder Kumar	08/11/2017
Signature of Reporting Person	Date

Explanation of Responses:

- * If the form is filed by more than one reporting person, see Instruction 4(b)(v).
- ** Intentional misstatements or omissions of facts constitute Federal Criminal Violations. See 18 U.S.C. 1001 and 15 U.S.C. 78ff(a).
- (1) Represents shares automatically sold pursuant to an irrevocable sell to cover election to satisfy tax withholding obligations in connection with the Reporting Person's Restricted Stock Unit ("RSU") vesting.
- Transaction executed in multiple trades at prices ranging from \$12.17 to \$12.92 per share, inclusive. The price reported in column 4 above reflects the weighted average sale price per share. The
- (2) Reporting Person hereby undertakes to provide the SEC staff, the Issuer or a security holder of the Issuer, upon request, full information regarding the number of shares sold at each respective price within the range set forth in this footnote.
- (3) Each RSU represents a contingent right to receive one share of AMD's common stock.
- (4) The RSUs vest 1/3 each on August 9, 2015, 2016 and 2017.
- (5) The RSUs vests 1/3 on each of August 9, 2017, 2018 and 2019.
- (6) The options vest 1/3 on each of August 9, 2018, 2019 and 2020.
- (7) The RSUs vest 1/3 each on August 9, 2018, 2019 and 2020.
- Each performance based restricted stock unit ("PRSU") represents a contingent right to receive one share of AMD's common stock. The number of PRSUs that may be earned is between 0% and 250%
- (8) of the target number of PRSUs; provided that, the maximum number of PRSUs that may be earned is capped at the number equal to (i) eight times the target value of the PRSU award, divided by (ii) the closing price of the Company's stock on the last day of a three-year performance period ending on August 9, 2020 (the "Performance Period").
- (9) The actual number of PRSUs that may be earned, if at all, is contingent upon the achievement of pre-established performance metrics over the Performance Period. Earned and vested PRSUs will generally be settled on the later of August 15, 2020, or the date following the Compensation Committee's certification of performance.

Note: File three copies of this Form, one of which must be manually signed. If space is insufficient, see Instruction 6 for procedure.

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