

STATEMENT OF CHANGES IN BENEFICIAL OWNERSHIP

OMB Number:	3235-0287
Estimated average burden hours per response:	0.5

Filed pursuant to Section 16(a) of the Securities Exchange Act of 1934 or Section 30(h) of the Investment Company Act of 1940

Check this box if no longer subject to Section 16. Form 4 or Form 5 obligations may continue. See Instruction 1(b).

Check this box to indicate that a transaction was made pursuant to a contract, instruction or written plan for the purchase or sale of equity securities of the issuer that is intended to satisfy the affirmative defense conditions of Rule 10b5-1(c). See Instruction 10.

1. Name and Address of Reporting Person* <u>Hahn Ava</u> (Last) (First) (Middle) 2485 AUGUSTINE DRIVE (Street) SANTA CLARA CA 95054 (City) (State) (Zip)	2. Issuer Name and Ticker or Trading Symbol <u>ADVANCED MICRO DEVICES INC [AMD]</u>	5. Relationship of Reporting Person(s) to Issuer (Check all applicable) Director 10% Owner <input checked="" type="checkbox"/> Officer (give title below) Other (specify below) <u>SVP, GC & Corporate Secretary</u>
	3. Date of Earliest Transaction (Month/Day/Year) 02/15/2024	
	4. If Amendment, Date of Original Filed (Month/Day/Year)	

Table I - Non-Derivative Securities Acquired, Disposed of, or Beneficially Owned

1. Title of Security (Instr. 3)	2. Transaction Date (Month/Day/Year)	2A. Deemed Execution Date, if any (Month/Day/Year)	3. Transaction Code (Instr. 8)			4. Securities Acquired (A) or Disposed Of (D) (Instr. 3, 4 and 5)			5. Amount of Securities Beneficially Owned Following Reported Transaction(s) (Instr. 3 and 4)	6. Ownership Form: Direct (D) or Indirect (I) (Instr. 4)	7. Nature of Indirect Beneficial Ownership (Instr. 4)
			Code	V	Amount	(A) or (D)	Price				

Table II - Derivative Securities Acquired, Disposed of, or Beneficially Owned (e.g., puts, calls, warrants, options, convertible securities)

1. Title of Derivative Security (Instr. 3)	2. Conversion or Exercise Price of Derivative Security	3. Transaction Date (Month/Day/Year)	3A. Deemed Execution Date, if any (Month/Day/Year)	4. Transaction Code (Instr. 8)		5. Number of Derivative Securities Acquired (A) or Disposed of (D) (Instr. 3, 4 and 5)		6. Date Exercisable and Expiration Date (Month/Day/Year)		7. Title and Amount of Securities Underlying Derivative Security (Instr. 3 and 4)		8. Price of Derivative Security (Instr. 5)	9. Number of derivative Securities Beneficially Owned Following Reported Transaction(s) (Instr. 4)	10. Ownership Form: Direct (D) or Indirect (I) (Instr. 4)	11. Nature of Indirect Beneficial Ownership (Instr. 4)
				Code	V	(A)	(D)	Date Exercisable	Expiration Date	Title	Amount or Number of Shares				
PRSU Award	(1)	02/15/2024		A		9,071		(2)	(2)	Common Stock	9,071	\$0	9,071	D	
RSU Award	(3)	02/15/2024		A		9,071		(4)	(4)	Common Stock	9,071	\$0	9,071	D	
RSU Award	(3)	02/15/2024		A		36,284		(5)	(5)	Common Stock	36,284	\$0	36,284	D	

Explanation of Responses:

- Between 0% and 200% of the target number of performance stock units ("PRSU") may be earned depending on (a) the return on AMD's stock price relative to the return on the S&P 500 Index over the performance period that begins August 9, 2023 and ends on August 9, 2026, (b) the absolute return on AMD's stock price over the performance period, and (c) the percentage (if any) by which AMD's 2025 fiscal year non-GAAP earnings per share exceeds AMD's 2023 fiscal year non-GAAP earnings per share. Vesting of any earned PRSUs is generally subject to the Reporting Person's continued employment and/or service with AMD through August 9, 2026 (or the one-year anniversary of a change in control, if earlier).
- The actual number of PRSUs that may be earned, if at all, will be determined by the Compensation and Leadership Resources Committee (the "Committee") based on AMD's actual performance with respect to the performance vesting conditions described in footnote 1, above. Earned and vested PRSUs will generally be settled on the later of August 15, 2026 or the date following the Committee's determination of performance.
- Each restricted stock unit ("RSU") represents a contingent right to receive one share of AMD's common stock.
- The RSUs vest 1/4 on each of February 15, 2025, 2026, 2027 and 2028.
- The RSUs vest in three tranches: (1) 6,047 on September 15, 2024; (2) 12,095 on January 15, 2025; and (3) 18,142 on January 15, 2026.

Remarks:

/s/Ava Hahn

02/19/2024

** Signature of Reporting Person

Date

Reminder: Report on a separate line for each class of securities beneficially owned directly or indirectly.

* If the form is filed by more than one reporting person, see Instruction 4 (b)(v).

** Intentional misstatements or omissions of facts constitute Federal Criminal Violations See 18 U.S.C. 1001 and 15 U.S.C. 78ff(a).

Note: File three copies of this Form, one of which must be manually signed. If space is insufficient, see Instruction 6 for procedure.

Persons who respond to the collection of information contained in this form are not required to respond unless the form displays a currently valid OMB Number.